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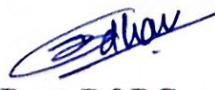
Department of Electronics & Communication (A.C.T.)

Research & Development Cell (R&DC) Activity Report

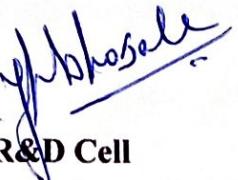
Name of the event:	Faculty Development Program on “Advanced Training on Printed Circuit Board Design”
Date of the event:	03-11-2025 to 07-05-2025
Organized by:	CSMSS Chh. Shahu College of Engineering
In Collaboration with:	National Institute of Electronics & Information Technology
Under the Initiative:	Research & Development Cell
Speaker:	Mr. PawanAlhat
Target Audience:	Faculty Members from outside & inside of CSMSS CSCOE.
Venue / Platform / Mode:	CSMSS CHH. SHAHU COLLEGE OF ENGINEERING, B- 504 (Offline)
Coordinator:	Prof.A.T.Jadhav
Objective:	Purpose of Learning Design PCB layout.
Overview	An offline session on “Advanced Training on Printed Circuit Board (PCB)”, was successfully conducted in association with National Institute of Electronics & Information Technology, Chh. Sambhajinagar. The session aimed to create awareness and understanding among faculties about PCB layout design, its significance in innovation, and the process of PCB designs.
Participation	26, faculties from various departments and colleges enthusiastically participated in the session, demonstrating high levels of interest and engagement
Fees:	No fees to attend the program- Certificates will be given to all participants who attended the programme.
Key Outcomes:	<ul style="list-style-type: none">➤ Participants gained practical knowledge of complete PCB design workflow — from schematic creation to Gerber file generation.➤ Faculty members performed real-time design exercises using professional PCB tools and AI-based software like Flux AI.➤ The FDP strengthened participants' ability to integrate modern PCB design techniques into academic teaching and student projects.➤ The program encouraged innovation and collaboration in advanced electronic design, fostering a base for future research initiatives.

Feedback & Suggestions:	Optional
Conclusion:	The session was highly informative and served as a steppingstone for faculties to understand and explore the knowledge of Layout and PCB design. The college and faculties and other outsider participants acknowledge the efforts of NIELIT and CSMSS CSCOE and the organizing team in making this session a success.
Photo:	 

Photograph taken during FDP on “Advanced Training on PCB design”


**Dept. R&DC
Coordinator**
 Prof. S. H. Jadhav


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