CSMSS
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Approved by AICTE New Delhi, DTE (Govt. of Maharashtra) and affiliated to Dr. BATU, Lonere (Raigad).   **DTE Code: 2533**

Department of Electronics Engineering (VLSI Design & Technology)

Classroom Details

Sr. No	Name	Capacity / Details	Total Area	Location
1	Class room - 1	70	54 sq. m	A-208
2	Class room - 2	70	54 sq. m	A-209