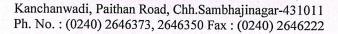


#### .CSMSS

# ChhatrapatiShahumaharajShikshanSanstha's CHH. SHAHU COLLEGE OF ENGINEERING





Approved by AICTE, New Delhi & DTE (Govt. of Maharashtra) & Affiliated to Dr. B.A.T.U. Lonere, Raigad

11 June 2025

## **NOTE**

# Request for Booking of CSMSS- NEW AUDITORIUM

Date	Name of event	Remarks
14th June	The department of "Electronics Engineering -(VLSI	Convener:-
2025	Design & Technology) & Centre for Engineering	-Dr.S.J.Honade,
it 10.00 am	Exploration of this college is organizing "IGNITE-2025"	HOD VLSI
to 05.00 pm	in association with ISTE. This is an ISTE (New Delhi	
	Approved Event).	Faculty
	This is an 'Engineering Project Exhibition –cum-	coordinators:-
	Competition 'in which participants' students of 2 <sup>nd</sup> , 3 <sup>rd</sup>	
	and 4th year of all branches will take part. Students of	1.Prof M.Tushar
	First year will also remain present during Inauguration	2.Prof P. Bhusar
	programme.	2.1 101 1 . Dilusar
	A copy of Pamphlet " IGNITE-2025 is enclosed for your	
	perusal and ready reference please.	
	-All teaching faculties of Depts. of VLSI &	
	EXPLORATION Depts. and other depts. not on any	
	essential duty will remain present.	
	The following staff will perform the duties given below:-	
	1. Sound system Operator, Generator Functioning:-	
	Mr. Malkar	
	2. Light effect & System Operator-Mr. V.S.Manghate	
	3. AC - Mr. Haribhau Gaikwad.	

(Dr.G.B.Dongre)
Principal

Encl: One

Copy to: - Dr. S. G. Deshmukh -AO Sanstha

- Mr. Anil S. Tayade - HR Officer Sanstha

Internal - Vice Principal, All HODs.(Reg-OC), All concerned, Mr. Tawar, Mr. Malkar,







### CHHATRAPATI SHAHU COLLEGE OF ENGINEERING

DEPARTMENT OF ELECTRONICS ENGINEERING (VLSI DESIGN & TECHNOLOGY) & CENTRE FOR ENGINEERING EXPLORATION

In association with

# Indian Society for Technical Education (ISTE)

organizing



## ENGINEERING PROJECT EXHIBITION-CUM-COMPETITI

#### An ISTE (New Delhi) Approved Event

Next Generation Competent Technocrats for Industry & Society 🔶

### **Event Details**







#### GUIDELINES

1) Group Size :-Minimum 3 to Maximum 5 members.

2) Project based on any problem statement is allowed & also both HARDWARE/SOFTWARE Projects are allowed.







#### ELIGIBILITY

All Departments Engineering Students of Year -

- 1st Year
- · 2nd Year
- 3rd Year
- 4th Year





- Rs. 5,000/-1) First Prize
- 2) Second Prize Rs. 3,000/-
- 3) Third Prize Rs. 2,000/-



#### REGISTRATION

- Registration Link (Google Form) :-[ https://forms.gle/hFVMos8ghaQBSz9r8 ]
  - NO REGISTRATION FEES
  - Registration Closure Date :-12th June 2025 (Thursday) at 5.15 PM





### DATE & TIME

14th June 2025 (Saturday) At 10:15 AM



#### **VENUE**

CSMSS, CSCOE B- Building (2nd & 3rd Floor)



#### Student Co-ordinators:-

- √ 7972907845 Mr. Prasad Desale
- € 84595 73602 Mr. Akshay Chalse
- 84599 71040 Mr. Sarvesh Jewalikar



### Faculty Co-ordinators:-

1) Prof. Tushar Mohije 2) Prof. Pankaj Bhusari



Dr. S. J. Honade

Mr. V. S. Damdhar Head. Centre for Engineering Exploration Dr. D. L. Bhuyar Vice-Principal

CSMSS, CSCOE

Dr. G. B. Dongre Principal CSMSS, CSCOE

#### **Organizers**

HoD, Electronics Engineering (VLSI D&T) ISTE Co-ordinator, CSMSS CSCOE