



**CSMSS**  
Chhatrapati Shahu Maharaj Shikshan Sanstha's  
**CHH. SHAHU COLLEGE OF ENGINEERING**  
**Department of Electronics & Communication**  
(Advanced Communication Technology)



Academic Year 2025-26

**Activity Report**

Date: 8-11-2025

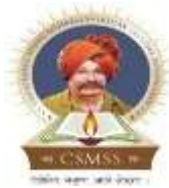
<b>Name of Activity</b>	Faculty Development Program
<b>Title of Activity</b>	<i>“Advanced Training on Printed Circuit Board (PCB) Design”</i>
<b>Association</b>	<b>National Institute of Electronics &amp; Information Technology</b>
<b>Date of Activity</b>	03-11-2025 to 07-05-2025
<b>Place of Activity</b>	CSMSS CHH. SHAHU COLLEGE OF ENGINEERING, B- 504.
<b>No of Participant</b>	26
<b>Program Coordinator</b>	Prof. A. T. Jadhav
<b>Outcome</b>	<ul style="list-style-type: none"><li>• Participants gained practical knowledge of complete PCB design workflow — from schematic creation to Gerber file generation.</li><li>• Faculty members performed real-time design exercises using professional PCB tools and AI-based software like Flux AI.</li><li>• The FDP strengthened participants’ ability to integrate modern PCB design techniques into academic teaching and student projects.</li><li>• The program encouraged innovation and collaboration in advanced electronic design, fostering a base for future research initiatives.</li></ul>



Principal Dr. G.B. Dongre Felicitating Resource Person Mr. Pawan Alhat



Principal Dr. G.B. Dongre Addressing participants



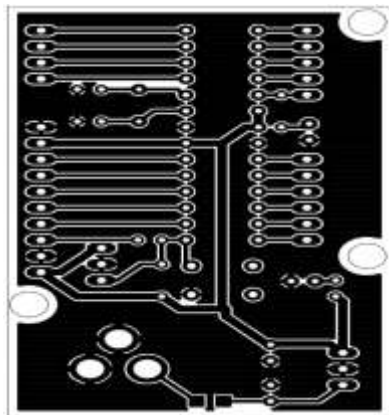
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Vice Principal Dr. D.L. Bhuyar Addressing participants



Session by Mr. Pawan Alhat



Layout Generated for Aduino Uno by participant



Certificate distribution to participants by Dr. Anirab from NIELIT

**Mr. A.T. Jadhav**  
Program Coordinator

**Dr. Sumera Ali**  
HOD

**Dr. G.B. Dongre**  
Principal