

ABOUT THE DEPARTMENT

The Electronics Engineering (VLSI Design and Technology) department has an infrastructure, and team of dedicated and experienced Faculty. Highly meritorious students are getting admitted to this program, not only from Marathwada but all over Maharashtra. In addition to core subjects related to the branch, VLSI core industry standard-specific and design-specific subjects are also included in the syllabus in line with the New Education Policy 2020. The department aims to develop VLSI-based industry-competent professionals of global standards capable of accepting challenges & able to solve societal problems at a larger level. The Department offers a undergraduate course- B.Tech. Electronics Engineering (VLSI Design and Technology) & Post Graduate Course - M.Tech. Electronics and Communication (VLSI Design) affiliated with Dr. Babasaheb Ambedkar Technological University, Lorene, Raigad. The department caters to the ever-increasing need of technical brilliance in all allied areas of electronics & VLSI Design.

ABOUT THE AICTE ATAL FDP

The FDP on "Next-Generation Semiconductor Devices: From FinFET to Advanced nsFET Design and Applications" focuses on modern transistor technologies shaping future VLSI systems.

It covers the evolution from CMOS to FinFET & advanced Nanosheet FET (nsFET/GAAFET) architectures. Participants will learn device design, performance analysis, fabrication trends, and practical applications.

OUTCOME OF THE WORKSHOP

Participants will understand the transition from CMOS to FinFET and advanced nsFET

technologies. Participants will gain knowledge of modern VLSI design, device modeling, and fabrication processes. Participants will explore current industry trends, research opportunities, and future semiconductor technologies. Participants will enhance their teaching, research, and technical skills in VLSI and semiconductor domains.

ELIGIBILITY CRITERIA

- Faculty members from AICTE-approved institutions in Engineering, Technology, and allied disciplines are eligible to apply. Assistant Professors, Associate Professors, Professors, and academic administrators can participate. Postgraduate students, Ph.D. scholars, and research scholars in relevant domains are eligible.
- Participants should be nominated/recommended by their respective Head of Institution as per ATAL guidelines.

MODE & REGISTRATION

- Registration is free. All the participants should register for the FDP using the link given below: - <https://atalacademy.aicte-india.org/>
- After successful registration, fill out the following Google Form <https://forms.gle/1Pk9MbXe2KgBDJmm6>
- Last date of registration: 10/12/2026 or before and selected candidates will get notification: 12-12-2026 (via email).
- For more information, please visit:
 - https://atalacademy.aicte.gov.in/assets/document/ATAL_offline/scheme_atal_fdp_offline_2026_27.pdf
 - https://drive.google.com/drive/folders/1Qqv0JlnSgiFd7nnwr99ZRTu_LKKkcDH?usp=drive_link

REGISTRATION ENQUIRIES

- Dr. Shrikant J. Honade, Coordinator (Mb. 9764250192, sjhonade@csmsengg.org)
- Mr. Ganesh G. Patil, Co-coordinator (9673363826, ggpatil@csmsengg.org)

One Week AICTE Training & Learning (ATAL) Academy Faculty Development Programme (Offline) Sponsored by



On

"Next-Generation Semiconductor Devices: From FinFET to Advanced nsFET Design and Applications"

From

14/12/2026 to 19/12/2026



Organized By



DEPARTMENT OF
ELECTRONICS ENGINEERING
(VLSI DESIGN & TECHNOLOGY)

CSMSS

CHH. SHAHU COLLEGE OF ENGINEERING

Approved by AICTE, New Delhi, Affiliated to Dr. Babasaheb Ambedkar Technological University, Lonere Kanchanwadi, Paithan Road, Chhatrapati Sambhajanagar (Aurangabad), MS, India - 431 011

<http://www.csmsengg.org/>

ABOUT THE COLLEGE

The Chhatrapati Shahu Maharaj Shikshan Sanstha's (CSMSS) Chhatrapati Shahu College of Engineering (DTE Code 2533) was established in the year 2013, aiming to develop the next generation of competent technocrats for industry and society. The institute is committed to ensuring the holistic development of students by imparting value and skill-based education, inculcation of skills and professional values, and providing a conducive system and environment for entrepreneurship, research, and innovation.

The Institute is located within the Municipal Corporation area of Chhatrapati Sambhajnagar (Aurangabad). It is located in the educational environment having various colleges such as Dental, Ayurved, Agriculture, Polytechnic, and Engineering, and is just 2 km from Chhatrapati Sambhajnagar Railway Station and 4 km from Central Bus Stand.

The college has eight Engineering Departments offering various B.Tech programs in ME, EE, CSE, Civil, AI & DS, Electronics & Computer Engineering, Electronics & Communication Engg. (ACT) and Electronics Engg. (VLSI Design & Technology (VDT)). The college runs seven M.Tech programs in CSE, AI & DS, EE, ME, Civil, ACT, and VLSI Design. Special attention is given to interdisciplinary research. The institute has an excellent placement record with several top-ranking companies visiting the campus every year.

Organizing Committee

Chief Patron

Hon. Prof. Yogesh Singh

Chairman, AICTE

Hon. Dr. Abhay Jere

Vice-Chairman, AICTE

Hon. Shri. Ranjeet Mulay

President CSMSS

Hon. Shri. Padmakarrao Mulay

Secretary, CSMSS

Patron

Dr. Shrikant Deshmukh

Administrative Officer, CSMSS

Chairperson

Dr. Ganesh Dongre

Principal, CSMSS, CSCoE

Co-Chairperson

Dr. Devendra Bhuyar

Dr. Sandeep Abhang

Vice Principal, CSMSS, CSCoE

Convener & Coordinator

Dr. Shrikant Honade

HoD, Electronics Engg. (VLSI Design & Tech.)

Co-Coordinator

Mr. Ganesh Patil

Assistant Professor, EE (VLSI D & T)

Organizing Team

Mr. T. A. Mohije, Mr. G. R. Bhalekar

About AICTE-ATAL

AICTE ATAL Academy is a leading initiative of AICTE aimed at enhancing the quality of technical education through faculty training and skill development programs. It conducts Faculty Development Programs in emerging and core areas to strengthen teaching, research, innovation, and industry readiness among educators and scholars. ATAL Academy supports NEP 2020 objectives by promoting modern pedagogy, technological advancement, and academic excellence across India.

One Week AICTE Training & Learning (ATAL) Academy
Faculty Development Programme (Offline)
Sponsored by



On

**"Next-Generation Semiconductor Devices:
From FinFET to Advanced nsFET Design
and Applications"**

From

14/12/2026 to 19/12/2026



Organized By

**DEPARTMENT OF
ELECTRONICS ENGINEERING
(VLSI DESIGN & TECHNOLOGY)**

CSMSS

CHH. SHAHU COLLEGE OF ENGINEERING

Approved by AICTE, New Delhi, Affiliated to
Dr. Babasaheb Ambedkar Technological University, Lonere
Kanchanwadi, Paithan Road, Chhatrapati Sambhajnagar
(Aurangabad), MS, India – 431 011

<http://www.csmssengg.org/>