

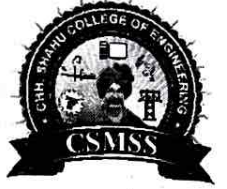


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CHHATRAPATI SHAHU MAHARAJ SHIKSHAN SANSTHA's  
CHH. SHAHU COLLEGE OF ENGINEERING

Kanchanwadi, Paithan Road, Chhatrapati Sambhajnagar 431 002 (M.S)

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Approved by AICTE New Delhi, DTE (Govt. of Maharashtra) and affiliated to Dr. BATU, Lonere (Raigad)

DTE Code : 2533

Date : 13/09/2025

## ELECTRONICS ENGINEERING (VLSI D&T) DEPARTMENT

### NOTICE

All are informed on **Engineer's Day**, the Department of Electronics Engineering (VLSI D & T) is pleased to organize a celebration under the theme "**Innovate & Celebrate 2025**". The program will include exciting events such as a **Treasure Hunt** to decode, explore and win surprises, **Engineer's Shark Tank** to pitch innovative ideas, and **Department Diaries** to relive our memories and moments. Winners will be awarded with **attractive prizes and e-certificates**.

Students are requested to register through the link: <https://forms.gle/oeM415bYfwsKCKwc9>. All students and faculty members are cordially invited to join us and celebrate the true spirit of engineering with fun, creativity, and innovation.

The details of the program are as follows:

- Date: 15th September 2025
- Venue: Classroom No. 204A
- Time: 3:00 PM to 5:15 PM



  
Dr. Shrikant Honade  
HOD, EE(VLSI D&T)



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Approved by AICTE New Delhi, DTE (Govt. of Maharashtra) and affiliated to Dr. BATU, Lonere (Raigad) DTE Code : 2533

Date : 7 Sep 2025

## ELECTRONICS ENGINEERING (VLSI D&T) DEPARTMENT

### NOTICE

This is to inform all the students and faculty members that the Department of Electronics Engineering (VLSI D & T) is organizing a Teachers' Day Celebration on the occasion of Teachers' Day-2025 . The event is being arranged to honour and appreciate the valuable contributions of our respected teachers towards education and student development. The program will include cultural performances, speeches, and fun activities dedicated to our teachers. All students and faculty members are cordially invited to attend the celebration and make the occasion memorable.

The details of the program are as follows:

- Date: 9th September 2025
- Venue: Classroom No. 204A
- Time: 1:00 PM to 3:00 PM



*Dr. Shrikant Honade*  
07/09/2025  
Dr. Shrikant Honade  
HOD, EE(VLSI D&T)

**CSMSS**  
**CHH. SHAHU COLLEGE OF ENGINEERING**  
**DEPARTMENT OF ELECTRONICS ENGINEERING (VLSI D&T)**

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Date: 08/03/2026

**Notice**

All the students of **Electronics Engineering (VLSI Design & Technology)** are hereby informed that the department is organizing a program to celebrate **International Women's Day** in order to recognize and appreciate the invaluable contributions of women in society, science, and technology. The celebration aims to promote the values of equality, respect, and empowerment, while inspiring students to acknowledge the significant role of women in shaping innovation and progress.

**Program Details:**

**Event:** International Women's Day Celebration

**Date:** 10 March 2026

**Time:** 11:00 AM

**Venue:** Seminar Hall









All students are requested to attend the program and participate enthusiastically to make the celebration meaningful and successful.



**Head of Department**

Electronics Engineering

(VLSI Design & Technology)

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TAN   
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